

Pateni

In re Patent Application of

Group Art Unit: 2827

Examiner: Ishwarbhai B. Patel

Confirmation No.: 7801

**For: CONNECTION METHOD AND
CONNECTION STRUCTURE OF PAD
ELECTRODES, AND INSPECTING
METHODS FOR CONNECTION STATE
THEREOF**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Enclosed is an Information Disclosure Statement and accompanying form PTO-1449 for the above-identified patent application.

- ☒ No additional fee for submission of an IDS is required.
- ☐ The fee of \$180.00 (1806) as set forth in 37 C.F.R. § 1.17(p) is also enclosed.
- ☒ A statement under 37 C.F.R. § 1.97(e) is also enclosed.
- ☐ A statement under 37 C.F.R. § 1.97(e), and the fee of \$180.00 (1806) as set forth in 37 C.F.R. § 1.17(p) are also enclosed.
- ☐ Charge \$_____ to Deposit Account No. 02-4800 for the fee due.
- ☐ A check in the amount of \$_____ is enclosed for the fee due.

The Director is hereby authorized to charge any appropriate fees under 37 C.F.R. §§ 1.16, 1.17 and 1.21 that may be required by this paper, and to credit any overpayment, to Deposit Account No. 02-4800. This paper is submitted in duplicate.

Respectfully submitted,—

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

Date: November 18, 2003

By: Ellen Marcie Emas
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Patent
Attorney's Docket No. 018976-203

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of)	
)	
Ryoichi MORIMOTO et al.)	Group Art Unit: 2827
)	
Application No.: 09/973,822)	Examiner: Ishwarbhai B. Patel
)	
Filed: October 11, 2001)	Confirmation No.: 7801
)	
For: CONNECTION METHOD AND)	
CONNECTION STRUCTURE OF PAD)	
ELECTRODES, AND INSPECTING)	
METHODS FOR CONNECTION STATE)	
THEREOF)	

SUPPLEMENTAL
INFORMATION DISCLOSURE STATEMENT AND CERTIFICATION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

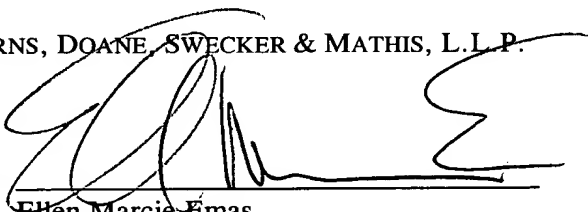
In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicants hereby submit the following English translation of Japanese Examination Report dated September 16, 2003 to a corresponding Japanese Application (copy enclosed) in conformance with 37 C.F.R. §§ 1.97 and 1.98. Please note that copies of the Japanese Examination Report along with copies of each document cited in the Report were previously submitted to the U.S. Patent and Trademark Office in an Information Disclosure Statement filed on October 15, 2003.

I, the undersigned, hereby certify that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

Date: November 18, 2003

By: 
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Processing code: 00534 MR

Issuance code: 303412 1/

Date issued: September 16, 2003

REJECTION NOTICE

Patent application no.: 2000-312192
Date prepared: August 25, 2003
Patent examiner: Kazuro SEKI 8223 4R00
Applicant's agent: Mr. Hitoshi NISHIZAWA
Applicable statute: Article 29, paragraph 2

The present application is being rejected for the reasons given below. Should you object, please submit a written response within 60 days of the date of issuance of this notice.

Reasons

Since, prior to filing of the present application, a person of ordinary skill in the field of art to which the invention pertains could have easily arrived at the invention described in the claims of the present application stated below, either based on inventions described in publications 1-3 below that were distributed in Japan or abroad or on inventions made available to the public over telecommunication lines prior to filing of the present application, pursuant to the Patent Law, Article 29, paragraph 2, no patent shall issue.

Record (for references, see Table of References)

Claims 1-6
References 1 and 2

Remarks

Inventions identical to the inventions of claims 1 to 6 are described in Reference 1. Moreover, the element described in claims 2 to 6 of making the width of the pad electrode on the component side wider than the width of the pad electrode on the base material side is clearly described in Reference 2.

Table of References

1. Patent Application Publication No. Hei 10 [1998]-074802
2. CD-ROM of Utility Model Application No. Hei 03 [1991]-075857 (Utility Model Application Publication No. Hei 05 [1993]-028037)

Record of Results of Prior Art Search

[sealed: [illegible]]

Processing code: 00534 MR

Issuance code: 303412 2/E
Date issued: September 16, 2003

Field(s) searched: IPC, 7th Edition, H01L 21/60

Prior art documents:

Patent Application Publication No. Hei 05 [1993]-243714

Patent Application Publication No. Hei 06 [1994]-268018

Patent Application Publication No. Hei 10 [1998]-163267

This Record of Results of Prior Art Search does not constitute grounds for rejection.

For inquiries relating to the contents of this Rejection Notice, or if an interview or the like is desired, please contact the following:

Kazuro SEKI, Metal and Electronic Materials Processing Examination Section, Division
3, Patent Examination

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